A More Precise Latency Bound of Deficit Round-Robin Scheduler

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Abstract. Delay is an important Quality of Service (QoS) parameter. A significant part of an end-to-end delay of a data flow is *latency* – the delay induced by scheduling algorithms in network devices. In this paper we present a latency analysis of a Deficit Round-Robin (DRR) scheduler. We correct inaccuracies and deficiencies of the DRR latency analysis detected in works of other authors. We derive a new and more precise latency bound of the DRR scheduler and show that, contrary to the latency bounds derived by other authors, our bound is mathematically correct. Finally we give a comparative analysis of the DRR latency bounds discussed in the paper.

Key words: DRR, Deficit Round-Robin, Latency, Latency Bound, Packet Networks, Scheduling

Natančnejša meja zakasnitve odpravnika z deficitarnim krožnim dodeljevanjem

Povzetek - Zakasnitev je pomemben parameter kakovosti storitve. Pomemben del zakasnitev od-konca-do-konca za posamezen podatkovni pretok je tudi začetna zakasnitev ali latenca – zakasnitev algoritma odpravnika v omrežnih napravah. V tem prispevku predstavljamo analizo latence odpravnika z deficitarnim krožnim dodeljevanjem (Deficit Round-Robin - DRR). Osvetlimo in odpravimo nepravilnosti in pomanjkljivosti analiz latence odpravnika DRR v delih drugih avtorjev. Izpeljemo novo in natančnejšo mejo zakasnitve odpravnika DRR in pokažemo, da je le-ta, v nasprotju z mejami zakasnitve drugih avtorjev, matematično pravilna. Za konec podamo primerjalno analizo mej zakasnitev odpravnikov DRR, obravnavanih v tem prispevku.

Ključne besede: DRR, odpravnik z deficitarnim krožnim dodeljevanjem, latenca, meja zakasnitve, paketno omrežje, odpravnik

1 Introduction

In present communication networks Quality of Service (QoS) is more and more important. QoS in communication networks is generally measured in terms of cell/packet loss, mean delay and delay jitter. Consequently the end-to-end delay is becoming a more important transmission parameter. It should be bounded, it should be as low as possible, and it should not have too much jitter. This is true especially for (interactive) realtime applications that do not tolerate high delays and jitter.

In packet networks resources are statistically shared among different traffic sources that try to send their data to the desired destinations. Overload causes congestion that is solved either by delaying or by dropping excess packets. Many of the problems that we face in networks are related to the allocation of a limited amount of shared resources (buffer, memory, bandwidth, etc.) to competing data flows. Solutions that try to solve this problem can be grouped into two categories: end-system-based solutions and network-based solutions. As latency* is a network device property, we are here only interested in the network-based solutions.

Many multimedia applications rely on the ability of the network to provide some sort of quality of service guarantees. QoS can generally be defined as a set of network mechanisms that satisfy the varied quality of service levels, while at the same time maximizing bandwidth utilization. Applications rely on traffic scheduling algorithms in network devices to guarantee performance bounds. Several measures are to be considered when choosing a scheduling algorithm. The most important are: latency, fairness and complexity. This paper focuses on latency.

The paper is organised as follows. In Section 2 we briefly describe why latency is an important property of each scheduler. In Section 3 we present our latency bound. We continue with the explanation of the basics of Deficit

^{*}Latency is defined in the following sections.

Round-Robin (DRR) scheduler in Section 4. Section 5 is the core of this paper where we give the definition of the latency and derive its upper bound for the Deficit Round-Robin scheduler. We conclude with a comparison of latency bounds of some well-known schedulers in section 6.

2 Importance of Delay and Latency

The end-to-end delay is a very important QoS parameter. A number of factors contribute to the end-to-end delay: forwarding delay, queuing delay, propagation delay and serialization delay. When scheduling algorithms are discussed, it is only the queuing delay that is of our interest. It denotes the amount of time that a packet has to wait in a queue as the system performs statistical multiplexing and while other packets are serviced before it can be transmitted on the output port.

The scheduling algorithm of a scheduler should provide end-to-end delay guarantees for individual flows* without severely under-utilizing network resources.

While queuing delay can be viewed primarily as a delay parameter of a packet(s), latency is a delay parameter associated to data flows. The notion of latency, that is going to be used here, is based on the length of time it takes *a new flow* to begin receiving service at its reserved rate (for details see [6]). Therefore, latency directly affects the size of the playback buffers required in real-time applications.

3 Our Latency Bound

Latency bounds of DRR have been given or derived in several papers that discuss Latency-Rate or Round-Robin like schedulers. Let us mention two of the more important papers written by Stiliadis and Varma [6] and Kanhere and Sethu [7]. Our derivation of the latency bound of the DRR has been inspired by both papers. We claim that our latency bound is more precise because some assumptions used to derive the latency bounds in [6] and [7] are incorrect.

In [6] they do not take into account the fact, that the packet length is a discrete value. It is true that for large maximum packet lengths this does not yield a big difference in latency bound, but for small maximum packet lengths it does. A more critical effect on the result has the inaccurate assumption about the maximal possible value of the deficit counter. In [7] they remediate both inaccuracies from [6], but they use irregular factor replacements in one of the inequalities in their derivation process. A detailed explanation of the mentioned inaccurate assumptions is given in [1] and [2].

In section 5 we give a simplified derivation of our latency bound. An exhaustive derivation with proofs can again be found in [1] and [2]. Further comments about the differences in latency bounds are given within Sections 5 and 6.

The most important result of our work presented in this paper is the latency bound that is more precise than the latency bounds given in [6] and [7].

4 Deficit Round-Robin

Scheduling algorithms can be broadly classified into two categories: sorted priority schedulers and frame-based schedulers.

Sorted priority schedulers maintain a global variable called the virtual time or system potential function. The priority of each packet, called the time-stamp, is calculated based on this variable. The packets are then scheduled in an increasing order of their time-stamps. Examples of sorted priority schedulers are Weighted Fair Queuing (WFQ), Self-clocked Fair Queuing (SCFQ), Starttime Fair Queuing (SFQ) and Worst-case Fair Queuing (WF2Q). Generally, they give good fairness and low latency bound but they have great computational complexity.

In frame-based schedulers, time is split into frames of fixed or variable length. Reservations are made in terms of the maximum amount of traffic the flow is allowed to transmit during a frame period. The service received by a flow in one round-robin opportunity is proportional to its fair share of the bandwidth. These schedulers do not have to perform sorting among packets and calculate global virtual time function, so they have lower computational complexity than the sorted priority schedulers do. Deficit Round-Robin (DRR), Surplus Deficit Round-Robin, Elastic Round-Robin, Nested Round-Robin are some of the frame based schedulers with complexity O(1), but they have worse fairness and latency properties than the sorted priority schedulers.

In 1996, Shreedhar and Varghese [5] proposed DRR, one of the most popular frame-based scheduling algorithms. The main characteristic of all DRR-like scheduling algorithms is their ability to provide guaranteed service rates for each flow (queue). DRR services flows in a strict round-robin order. It has complexity O(1) and it is easy to implement. Its latency is comparable to other frame-based schedulers. A detailed operation of the DRR algorithm can be found in [5].

^{*}Traditionally, a flow is defined as a sequence of packets generated by the same source and headed toward the same destination via the same path in the network. It is assumed that packets belonging to different flows are queued separately while they await transmission. A scheduler dequeues packets from these queues and forwards them for transmission.

Below is a list of variables used in our analysis:

R	transmission rate of an output link,
N	total number of active flows,
r_i	reserved rate of flow <i>i</i> ,
w_i	weight assigned to each flow <i>i</i> ,
Q_i	quantum assigned to flow <i>i</i> ,
DC_i	deficit counter of flow i ,
F	frame size,
L_{max}	maximum possible packet size.

Because all flows share the same output link, a necessary constraint is that the sum of all reserved rates must be less or equal to the transmission rate of the output link:

$$\sum_{i} r_i \le R \tag{1}$$

Let r_{min} be the smallest of r_i : $r_{min} = \min_{\forall i} r_i$. Each flow *i* is assigned a weight that is given by:

$$w_i = \frac{r_i}{r_{min}}.$$
 (2)

Note that $\forall i \in 1, 2, \dots, N$ holds $w_i \geq 1$. Each flow i is assigned a quantum of Q_i bits, that is a whole positive value, i.e. $Q_i \in \mathcal{N}$. This quantum is actually the amount of service that the flow should receive during each round-robin service opportunity. Let us define with Q_{min} the minimum of all the quanta. Then the quantum for each flow i is expressed as:

$$Q_i = w_i Q_{min}.$$
 (3)

5 Latency

Stiliadis and Varma in [6] defined a general class of schedulers, called Latency-Rate (\mathcal{LR}) servers. The behaviour of an \mathcal{LR} server is determined by two parameters – the *latency* and the *allocated rate*. Latency of an \mathcal{LR} server is the worst-case delay seen by the first packet of a busy flow. That is the packet arriving when the flow's queue is empty.

The latency of a particular scheduling policy may depend on its internal parameters: its transmission rate on outgoing link, the number of flows sharing the link and their allocated rates.

In this definition of \mathcal{LR} servers, there was no assumption made on whether the server is based on a fluid model or a packet-by-packet model. The only requirement is that a packet is not considered as departing the server until its last bit has departed. Therefore, packet departures must be considered as impulses. The DRR algorithm satisfies all of these assumptions.

The authors also developed and defined the notion of latency of the scheduling algorithm and determined an upper bound on the latency for a number of schedulers that belong to a class of \mathcal{LR} servers. This notion of latency is based on the length of time it takes a new flow to begin receiving service at its guaranteed rate.

Using the general idea of Stiliadis and Varma in [6] we derive the upper latency bound for the DRR algorithm that is different from theirs. It is also different from the bounds derived in [7]. We show that our upper bound is mathematically correct contrary to the ones derived in [6] and [7]. A more detailed analysis is given in [1] and [2].

Let us first define active and busy periods of a flow.

Definition 1 An active period of a flow is the maximal interval of time during which the flow has at least one packet awaiting service or in service.

Definition 2 A busy period of a flow is the maximal time interval during which the flow would be active if served exactly at its reserved rate.

An active period reflects the actual behaviour of the scheduler since the service offered to a flow varies depending on the number of active flows. A busy period is a mathematical construction that tells us how long a flow would be active if served at exactly its reserved rate.

Let us define the following parameters:

α_i	start of a busy period for flow i
$t > \alpha_i$	time instant such that flow i is
	continuously busy during the time
	interval (α_i, t)
$A_i(\alpha_i, t)$	arrivals from flow i during the time
	interval (α_i, t)
$SENT_i(\alpha_i, t)$	amount of service received by
	flow <i>i</i> during time interval (α_i, t)
$S_i(\alpha_i, t)$	number of bits belonging to packets
	in flow <i>i</i> that arrived after time α_i
	and are scheduled during the time

 au_{j} interval (α_{i}, t) au_{j} starting time of the *j*-th busy period of flow

$$\tau_j^*$$
 time instant at which the last
bit of traffic, arrived during the
j-th busy period, leaves the server
number of bits belonging to packets
in flow *i* that arrived at the *j*-th
busy period, i.e. in the time interval
 (τ_i, t)

Note that $S_i(\alpha_i, t)$ and $SENT_i(\alpha_i, t)$ are not the same. During the given time interval, the scheduler may still be servicing packets that arrived during a previous busy period, and thus $S_i(\alpha_i, t)$ and $SENT_i(\alpha_i, t)$ are not the same values. In fact the following must hold: $S_i(\alpha_i, t) \leq SENT_i(\alpha_i, t)$. For Stiliadis and Varma a server S belongs to a class of \mathcal{LR} servers if and only if for all time instants t after α_i :

$$S_i^j(\alpha_i, t) \ge \max\{0, r_i(t - \alpha_i - \theta_i)\}$$

$$\tag{4}$$

where θ_i is the minimum non-negative number that satisfies the above inequality and represents an upper latency bound.

The previous definition of latency is based on flow busy periods. In practice it is easier to analyze scheduling algorithms based on flow active periods. The following was proven in [1] and [2]:

Lemma 1 Let (α_i, t) indicate an interval of time in which flow *i* is continuously busy. If the service offered to the packets that arrived in the interval (α_i, t) can be bounded at every instant *s*, $\alpha_i < s \leq t$ as

$$S_i(\alpha_i, s) \ge \max\{0, r_i(s - \alpha_i - \theta_i)\}$$
(5)

then this server is a \mathcal{LR} server with a latency less than or equal to θ_i .

The following definition gives us a proper tool for the analysis of the latency bound:

Definition 3 The latency of flow *i* is the minimal nonnegative constant θ_i that satisfies, for all busy periods of the flow, the following inequality:

$$S_i(\alpha_i, t) \ge \max\{0, r_i(t - \alpha_i - \theta_i)\}$$
(6)

Definition 4 θ'_i is the smallest non-negative constant such that the following expression holds:

$$SENT_i(\beta_i, t) \ge \max\{0, r_i(t - \beta_i - \theta_i')\}$$
(7)

where β_i is an instant of time when flow *i* becomes active and $t > \beta_i$ is the time instant such that flow *i* is continuously active during the entire interval (β_i, t) .

Corollary 1 Even though (β_i, t) may not be a continuously busy period for flow *i*, the following holds:

$$\theta_i \le \theta_i' \tag{8}$$

i.e., latency θ_i , as defined earlier, is bounded with θ'_i , for every flow *i*.

Theorem 1 The DRR scheduler belongs to the class of the \mathcal{LR} servers with an upper bound on latency θ_i for flow *i* given with the following expression:

$$\theta_{i} \leq \theta_{i}' \leq Q_{i} \left(\frac{1}{r_{i}} - \frac{1}{R}\right) + (L_{max} - 1) \left(\frac{Q_{i}}{Fr_{i}}N + \frac{1}{r_{i}} - \frac{2}{R}\right)$$
(9)

Proof 1 Since the latency of a \mathcal{LR} server can be estimated based on its behaviour in the flow active period, see Corollary 1, we will prove the theorem by showing that,

$$\theta_i \leq Q_i \left(\frac{1}{r_i} - \frac{1}{R}\right) + (L_{max} - 1) \left(\frac{Q_i}{Fr_i}N + \frac{1}{r_i} - \frac{2}{R}\right) \quad (10)$$

This part of the proof is the same as in [7] and will not be repeated here. So the reader should fill this gap with the work in [7] until the following expression:

$$SENT_{i}(\beta_{i},\beta_{i}^{k}) \geq \frac{Q_{i}R}{F} \left[(\beta_{i}^{k} - \beta_{i}) - \frac{F - Q_{i}}{R} - \frac{(N-1)(L_{max} - 1)}{R} + \frac{L_{max} - 1}{R} \left(1 - \frac{F}{Q_{i}} \right) \right]$$
(11)

This statement is true but the results following it in [7] are not. Discussion is given in Comment 1.

Now, since it is proven in [5], that $r_i \leq \frac{Q_i}{F}R$, the factor $\frac{Q_i}{F}R$ in inequality (11) can only be replaced by r_i when the expression in square brackets is greater than or equal to zero. We do not know the exact value of that expression. To proceed correctly, we must dispart the expression (11). We get:

$$SENT_{i}(\beta_{i},\beta_{i}^{k}) \geq \frac{Q_{i}R}{F}(\beta_{i}^{k}-\beta_{i})-Q_{i} +$$

$$+ \frac{Q_{i}R}{F}\frac{Q_{i}}{R} -$$

$$- \frac{Q_{i}N(L_{max}-1)}{F} +$$

$$+ \frac{Q_{i}(L_{max}-1)}{F} -$$

$$- (L_{max}-1) \qquad (12)$$

We can now use the replacement only on the positive elements of the right side of the inequality. Doing so, we decrease the value of the right side of the inequality, and the inequality still holds. However, when we use the replacement on the negative elements, we increase the value of the right side of the inequality, and the inequality does not necessarily hold anymore. Applying the above methodology, we get the following inequality:

$$SENT_{i}(\beta_{i},\beta_{i}^{k}) \geq r_{i}(\beta_{i}^{k}-\beta_{i})-Q_{i}+$$

$$+ Q_{i}\frac{r_{i}}{R}\frac{Q_{i}N}{F}-(L_{max}-1)+$$

$$+ \frac{r_{i}}{R}(L_{max}-1)+$$

$$+ (L_{max}-1)\left(\frac{r_{i}}{R}-1\right)$$
(13)

From the previous expression we get:

$$SENT_{i}(\beta_{i},\beta_{i}^{k}) \geq r_{i} \left[(\beta_{i}^{k} - \beta_{i}) - Q_{i} \left(\frac{1}{r_{i}} - \frac{1}{R} \right) - (L_{max} - 1) \left(\frac{Q_{i}}{Fr_{i}}N + \frac{1}{r_{i}} - \frac{2}{R} \right) \right]$$
(14)

Let us now compare inequalities (7) and (14). Taking into consideration that θ'_i is defined as the smallest nonnegative constant, that satisfies (7), and that t in (7) is replaced by β^k_i we get the following inequality:

$$\theta_i' \leq Q_i \left(\frac{1}{r_i} - \frac{1}{R}\right) + (L_{max} - 1) \left(\frac{Q_i}{Fr_i}N + \frac{1}{r_i} - \frac{2}{R}\right) \quad (15)$$

where θ'_i is the minimal non-negative constant that satisfies the inequality (7). Finally, from the inequality (8) we get our latency bound:

$$\theta_{i} \leq Q_{i} \left(\frac{1}{r_{i}} - \frac{1}{R}\right) + \left(L_{max} - 1\right) \left(\frac{Q_{i}}{Fr_{i}}N + \frac{1}{r_{i}} - \frac{2}{R}\right) \quad (16)$$

Comment 1 Let us return for the moment to expression (11) and the value of the expression in the square brackets on its right side:

$$(\beta_i^k - \beta_i) - \frac{F - Q_i}{R} - \frac{(N-1)(L_{max} - 1)}{R} + \frac{L_{max} - 1}{R} (1 - \frac{F}{Q_i})$$
(17)

To prove their Theorem 1 in [7], from the expression (17) on, Kanhere and Sethu linearly used the inequality $r_i \leq \frac{Q_i}{F}R$. As already explained in the proof of our Theorem 1, this is not mathematically correct and cannot be done in this way. We can make the replacement only on the positive factors of the disparted version of the expression (17).

For the validity of the proof in [7] it is necessary that the expression (17) is always positive for every combination of its parameters. Since this is not always satisfied, their result is incorrect. The counter example goes like this: for a chosen value of parameters R = 4000, N = 10, $Q_i = 200$, $L_{max} = 1500$, k = 3, where k represents the number of rounds, the expression (17) has a negative value of -3.023. For the calculation of time $\beta_i^k - \beta_i$ we used the expression

$$k\frac{F}{R} + \frac{(N-1)(L_{max}-1)}{R}$$

since this interval represents the maximal possible amount of time used for the k round-robin service rounds.

It can therefore be observed that the latency bound in [7] will stand for the most of the values of k and Q_i . However, it does not stand for every k and Q_i , precisely it is not going to stand when the small number of rounds in round-robin servicing is observed or when Q_i is a small value. Since their latency bound does not stand for all cases, it is not correct.

If we look deeper into this difference among the discussed bounds, we can see that our bound gets relatively closer to the one derived in [7] as r_i , and with that also Q_i , get smaller. Clearly, this difference gets bigger as r_i and Q_i get bigger.

6 Comparison of Latency Bounds

Table 1 shows latency bounds of various scheduling algorithms. Although it is not easy and straightforward to compare the latency bound of the DRR scheduler to the latency bounds of other listed schedulers, the table can give us a general idea of the latency bounds of the most popular schedulers.

It is also not straightforward to compare the three different latency bounds for the DRR scheduler. While the bound derived in [6] has only three parameters (F, Q_i and R), the bound derived in [7] introduces additional two parameters (N and L_{max}), and our bound another one (r_i). In general, the bound from [7] is tighter than the one from [6], and our bound is between the both. Some figures comparing the bounds are available in [2].

7 Conclusion

Stiliadis and Varma in [6] defined a general class of schedulers, called Latency-Rate (\mathcal{LR}) servers. Using the general idea of Stiliadis and Varma [6] and of Kanhere and Sethu [7], we derived the upper latency bound for the DRR scheduling algorithm. We show that our bound is unique regardless of the approach used.

Scheduler	Latency bound
GPS	0
FIFO	∞
PGPS =WFQ	$\frac{L_{max}}{R} + \frac{L_{max}}{r_i}$
Virtual Clock Fair Queuing	$\frac{L_{max}}{R} + \frac{L_{max}}{r_i}$
SCFQ	$\frac{(N-1)L_{max}}{R} + \frac{L_{max}}{r_i}$
Frame-based Fair Queuing	$\frac{L_{max}}{R} + \frac{L_{max}}{r_i}$
Weighted Round-Robin	$\frac{1}{R}(F - Q_i + L_{max})$
Elastic Round-Robin	$\frac{1}{R}\left((F - Q_i) + (L_{max} - 1)(N - 1)\right)$
DRR - as in [6]	$\frac{3F - 2Q_i}{R}$
DRR - as in [7]	$\frac{1}{R}\left(\left(F-Q_i\right)+\left(L_{max}-1\right)\left(\frac{F}{Q_i}+N-2\right)\right)$
DRR - our result	$Q_i(\frac{1}{r_i} - \frac{1}{R}) + (L_{max} - 1)\left(\frac{1}{r_i} - \frac{2}{R} + \frac{Q_i}{Fr_i}N)\right)$

Table 1. Comparison of latency bounds among different schedulers. Denotations used: transmission rate of an output link (R), total number of active flows (N), reserved rate of flow $i(r_i)$, weight assigned to each flow $i(w_i)$, quantum assigned to flow $i(Q_i)$, Deficit Counter of flow $i(DC_i)$, frame size (F), maximum packet length (L_{max}) .

It should be noted that depite using the same ideas as Stiliadis and Varma, and Kanhere and Sethu, we made some changes and corrections in the derivation leading to a new and mathematically correct latency bound.

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